



ATE1-71 TEC Modules

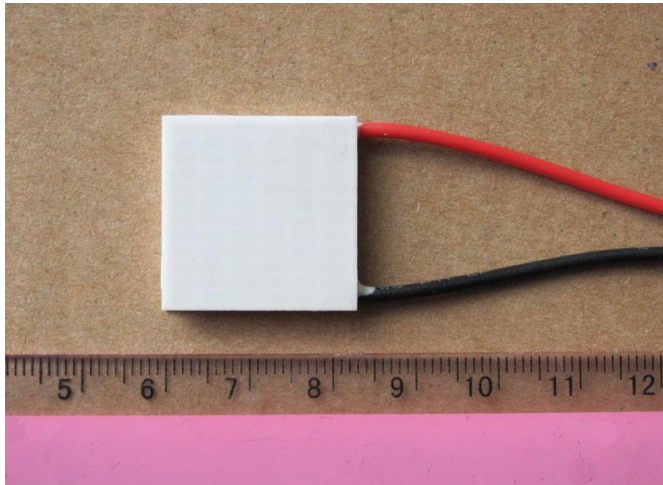


Figure 1.1 The Photo of Actual ATE1-71

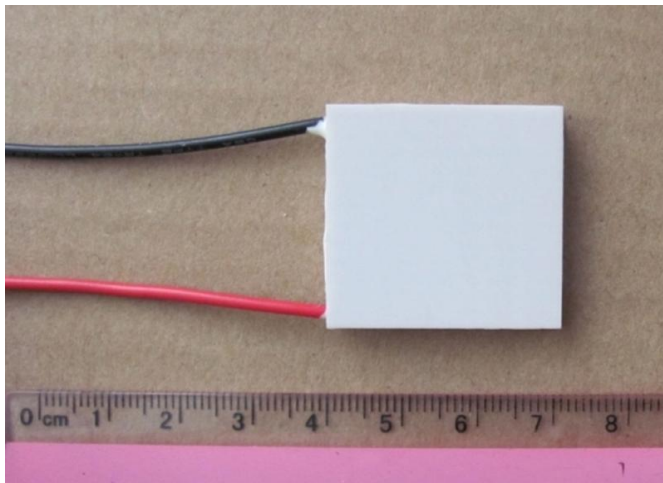


Figure 1.2 The Photo of Actual ATE1-71

FEATURES

- ➔ Maximum Input Voltage: 8.5V
- ➔ Low Cost
- ➔ Long Life
- ➔ 100 % lead (Pb)-free and RoHS compliant

APPLICATIONS

Regulate the temperature of the target object with high changing speed and stabilize the temperature to a wide range with high precision. Widely used in solid state laser, icebox, air condition, IC's, optical components, CPU's, CCD cameras, etc.

DESCRIPTIONS

This series of TEC (Thermoelectric Cooler) modules, ATE1-71, has 71 pairs of Peltier elements inside with a maximum voltage of 8.5 Volt, there are several options for maximum current, resulting in different power gratings, as shown in

Table 1 below. They are designed for precisely regulating the temperature of the target objects and can be controlled by our TEC controllers to build highly stable and efficient temperature regulating systems. The ATE1-71 series TECs can be used with our thermistors as well to achieve precise and stable temperature sensing.

The ATE1-71 series TECs come with highly flat bare ceramic surfaces on both sides. They can be mounted onto flat metal surfaces by inserting thin layers of thermally conductive filler materials, the so-called thermal pads, or placing a thin layer of thermal paste. When mounting, make sure that proper pressure is applied constantly to keep good thermal contacting between the metal and the TEC plates, minimizing thermal resistance between them.

The TECs can withstand strong orthogonal forces applied to the surface, but very vulnerable to tangent forces, especially shocking tangent forces. A small shocking tangent force can cause the Peltier elements to crack inside. The crack may not cause operation problems initially, but it will grow with time, causing the TEC resistance to increase slowly, by the end, the TEC will stop operating.

In the part number, for example, ATE1-71-xASH, “xA” indicates the maximum current allowed for entering the TEC module. “S” represents a sealed version and “H” stands for high operating temperature, up to 200°C. All of them can achieve a maximum temperature difference, DT_{MAX} , of 74.5°C.

There are two different operating temperature ranges to choose. One is -55°C~85°C for the TECs, whose part number is without an “H” and the other is -55°C~200°C for the TECs, whose part number ends with an “H”.

The ATE1-71 TECs come with 2 insulated lead wires. The positive wire is red, and the negative wire is black. The mechanical dimensions are shown in Figure 14, Figure 15 and Table 1.

The TECs, which have the edge area sealed, can prevent moisture from getting into the Peltier elements, so that the life of the TECs is extended.

The advantage of the non-sealed TECs is that the efficiency is higher and can achieve higher maximum temperature difference between the two TEC plates. Figure 1.1 and 1.2 show the actual sealed and non-sealed TECs.

For applications in moisture environments, our sealed version is recommended, in order to achieve long life and high reliability for the system.

For high end applications where good and reliable thermal contacts are needed between the TEC and the target object surfaces, the TEC ceramic surface can be metalized so that the TEC and the target object surfaces can be soldered together.

SPECIFICATION
Table 1

Part #	I _{MAX} (A)	V _{MAX} (V)	Q _{MAX} (W)	DT _{MAX} (°C)	Dimension (mm)				Note	Buy Now
					L _c	L _h	W	H		
ATE1-71-1AS	1	8.5	5.6	63	23		23	5.07	Sealed	 *  *
ATE1-71-1.9A	1.9	9	11.7	74.5	18		18	3.8	Non- sealed	 *  *
ATE1-71-2AS	2	8.5	11.2	63	23		23	3.69	Sealed	 *  *
ATE1-71-2BS	2	8.5	11.2	63	30		30	4.20	Sealed	 *  *
ATE1-71-3A	3	8.5	15.8	64	23		23	3.34	Non-sealed	 *  *
ATE1-71-3AS	3	8.5	15.8	63	23		23	3.60	Sealed	 *  *
ATE1-71-3B	3	8.5	16.6	64	30		30	4.80	Non-sealed	 *  *
ATE1-71-3BS	3	8.5	16.8	63	30		30	4.80	Sealed	 *  *
ATE1-71-4A	4	8.5	21.1	64	23		23	3.20	Non-sealed	 *  *
ATE1-71-4AS	4	8.5	21.1	63	23		23	3.20	Sealed	 *  *
ATE1-71-4B	4	8.5	21.1	64	30		30	4.40	Non-sealed	 *  *
ATE1-71-4BS	4	8.5	22.4	63	30		30	4.40	Sealed	 *  *
ATE1-71-5A	5	8.5	26.3	63	23		23	3.18	Non-sealed	 *
ATE1-71-5AS	5	8.5	26.3	63	23		23	3.18	Sealed	 *  *
ATE1-71-5B	5	8.5	29.1	64	30		30	4.11	Non-sealed	 *  *
ATE1-71-5BS	5	8.5	29.1	64	30		30	4.11	Sealed	 *  *
ATE1-71-6AS	6	8.5	34.1	63	30		30	4.00	Sealed	 *  *
ATE1-71-7AS	7	8.5	40.3	63	30		30	3.50	Sealed	 *  *
ATE1-71-8AS	8	8.5	44.8	63	30		30	3.40	Sealed	 *  *
ATE1-71-2BSH	2	8.5	11.2	63	30		30	4.20	Sealed	 *  *
ATE1-71-3BH	3	8.5	16.6		30		30	4.80	Non-sealed	 *  *
ATE1-71-3BSH	3	8.5	16.8		30		30	4.80	Sealed	 *  *
ATE1-71-4BH	4	8.5	21.1		30		30	4.40	Non-sealed	 *  *
ATE1-71-4BSH	4	8.5	22.4		30		30	4.40	Sealed	 *  *
ATE1-71-5BH	5	8.5	29.1		30		30	4.11	Non-sealed	 *  *
ATE1-71-5BSH	5	8.5	28	63	30		30	4.11	Sealed	 *  *
ATE1-71-6ASH	6	8.5	34.1		30		30	4.08	Sealed	 *  *
ATE1-71-7ASH	7	8.5	40.3		30		30	3.68	Sealed	 *  *
ATE1-71-8ASH	8	8.5	44.8		30		30	3.60	Sealed	 *  *

* DT_{MAX}: DT stands for Differential Temperature between TEC's 2 plates

APPLICATION INFORMATION

As shown in Table 1, the DT_{MAX} , the maximum temperature difference between the 2 TEC plates, is between 63°C to 74.5°C. This is the normal value for a single stage TEC module. When needing a higher DT_{MAX} , 2 or 3 stage TECs must be utilized. Contact us for details.

TEC modules can be used for stabilizing laser chip temperature, to stabilize the wavelength and the working lasing mode, resulting in less or no mode hopping and stable output power.

Inversely, when applying a temperature difference between the TEC 2 plates, electricity can be generated. Thus, the TECs can be called TEGs (thermoelectric Generators).

When designing a thermal system by using TECs, one should choose the TEC module in the following way:

1. To achieve the maximum efficiency, it is essential to minimize the thermal resistance between the TEC plate surface and heat sink surface and between the TEC plate and the target object surface. The best way to minimize the thermal resistance is to mount the TEC modules' plates to the heat-sink and to the thermal load by soldering them together. This requires metalizing the TEC plate surfaces first. The 2nd best way is to apply a thin layer of thermal paste between the TEC plates and the heat-sink and the target object surfaces. Constant pressure is needed between the TEC plates and heat-sink and the target object surfaces. Thermal pad material, or so called thermal filler pads, can be used to replace the thermal paste. But this may increase the thermal resistance between the TEC plates and the heat-sink and

the target object surfaces. Therefore, thermal paste is recommended to be applied between the TEC plates and the heat-sink. The 3rd best way is to use thermally conductive epoxy, to glue the TEC surface and the heat-sink and the target object surfaces together. This approach is the least reliable because the epoxy may lose its adhering power as time goes.

2. To achieve high COP, Coefficient of Performance, which is defined as:

$$COP = \text{thermal power} / \text{electric power},$$

the ratio between the TEC's output thermal power and the input electric power. Apparently, a high COP leads to low power system consumption, thus, high efficiency. The key to achieve high COP is to design the system with a low maximum temperature difference between the 2 TEC plates (the hot side and the cold side), DT . When the operating DT can be kept to be $\leq 30^\circ\text{C}$, the COP can be as high as 2, a very good result.

3. When the required maximum temperature difference is low, such as $< 30^\circ\text{C}$, a large TEC module can be used to drive a small thermal load, resulting in a low DT , thus high COP and efficiency.
4. It is not hard to design in a TEC system, but does require some understanding of heat transfer and a good grasp of your applications.
5. Use the charts provided in Figure 2 to Figure 9, to decide which TEC to use, what is the heat needed to be dumped through the heat sink, what is the heat sink temperature, etc.



TYPICAL CHARACTERISTICS

1. $dT_{MAX} = 74.5^{\circ}C$

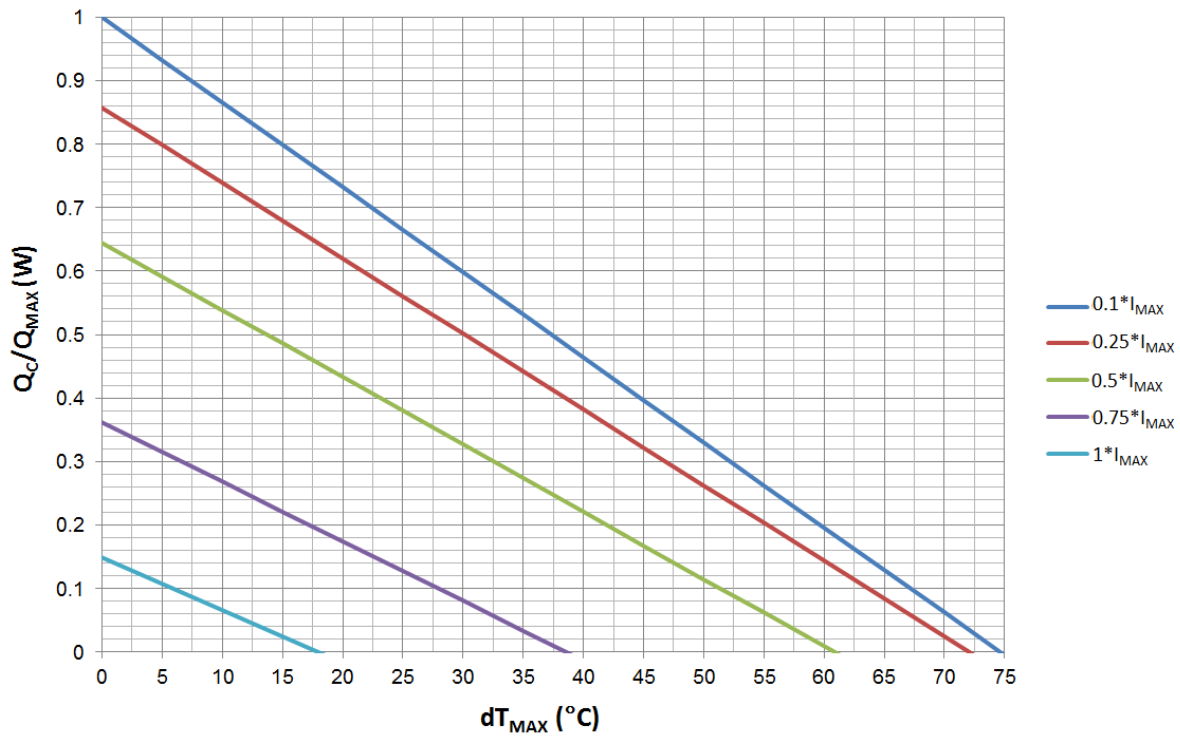


Figure 2. Q_c/Q_{MAX} vs. dT_{MAX}

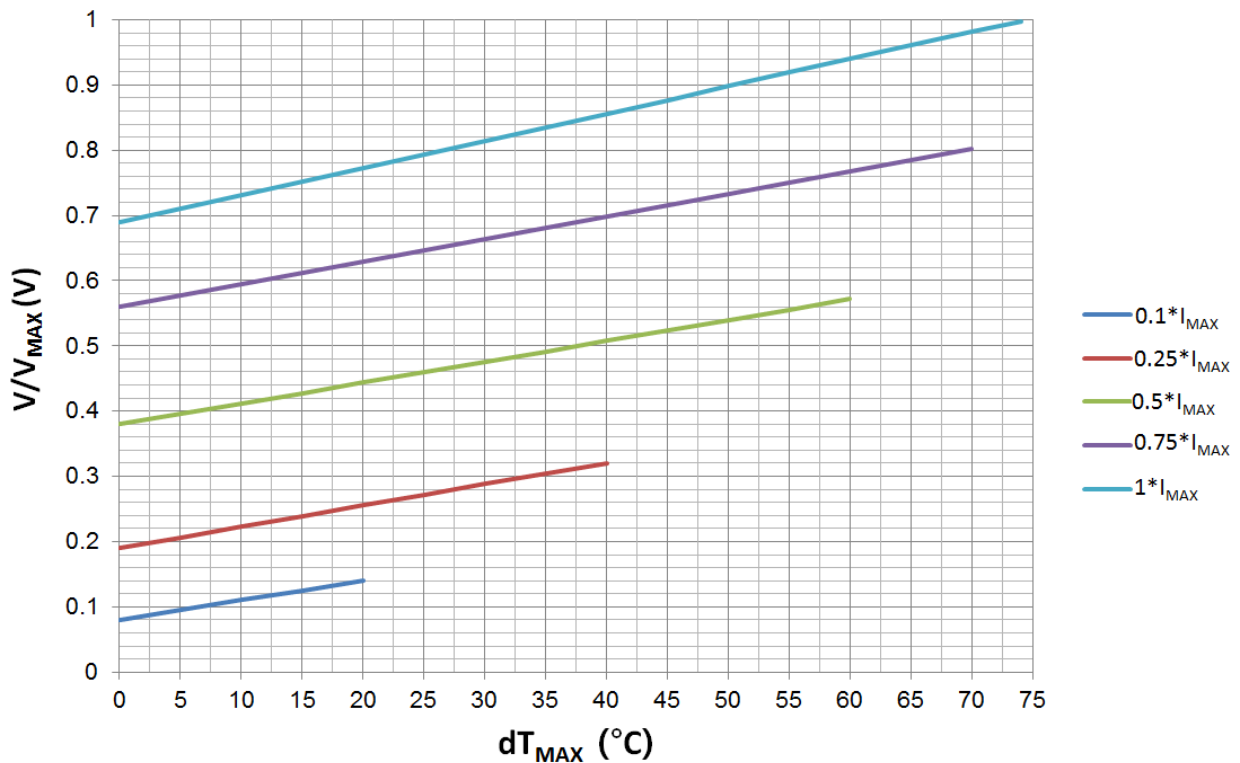


Figure 3. V/V_{MAX} vs. dT_{MAX}

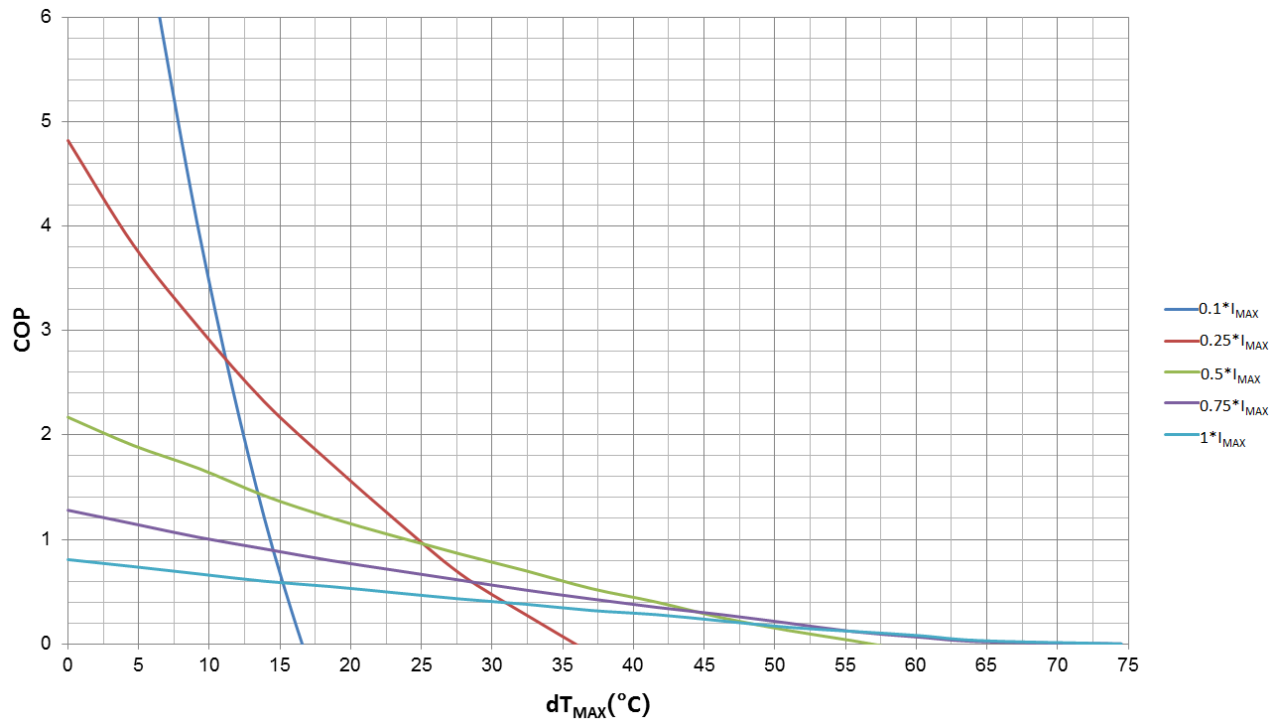


Figure 4. COP vs. dT_{MAX}

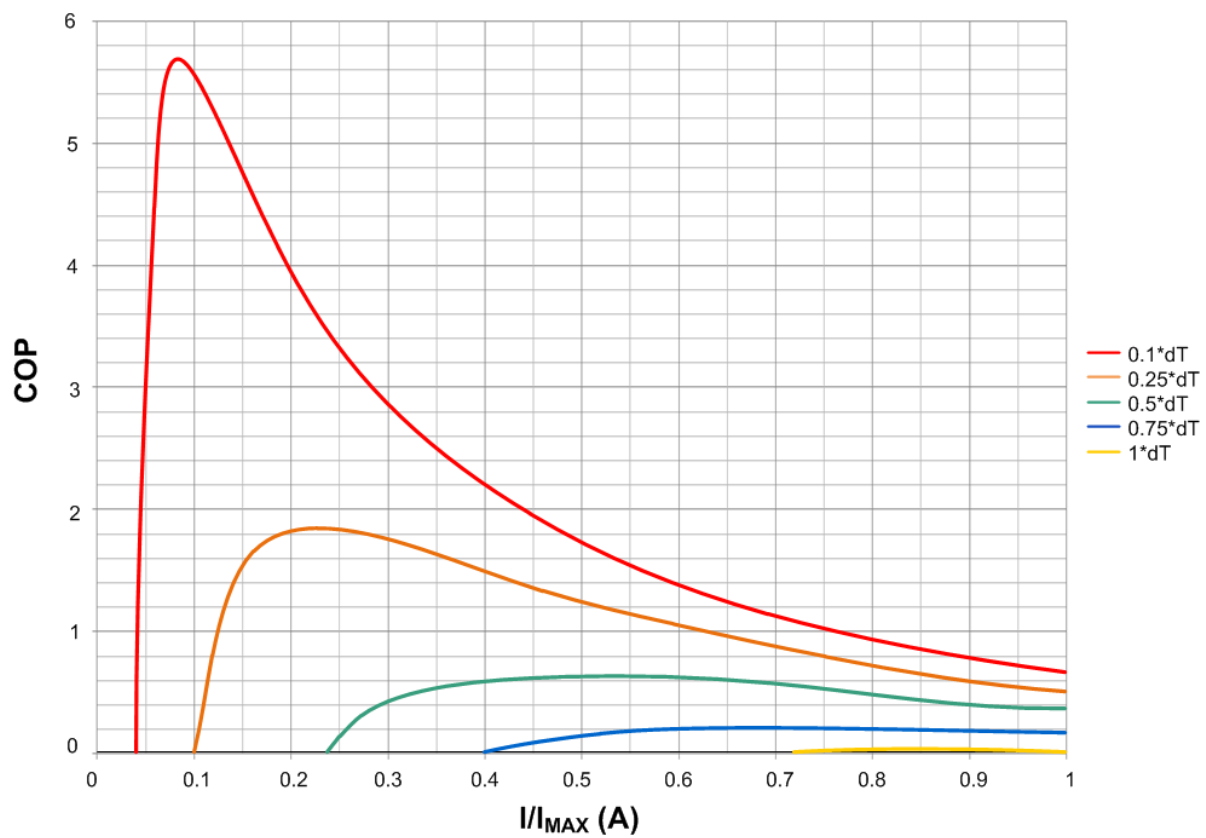


Figure 5. COP vs. I/I_{MAX}



2. $dT_{MAX} = 64^{\circ}C$

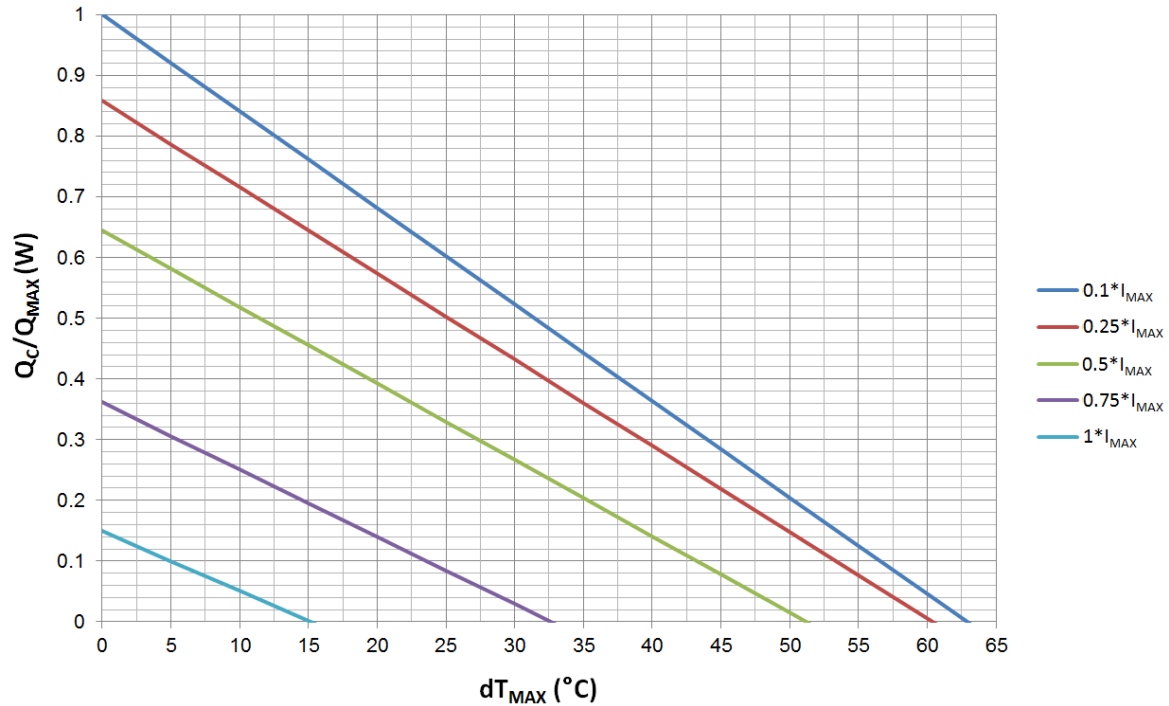


Figure 6. Q_c/Q_{MAX} vs. dT_{MAX}

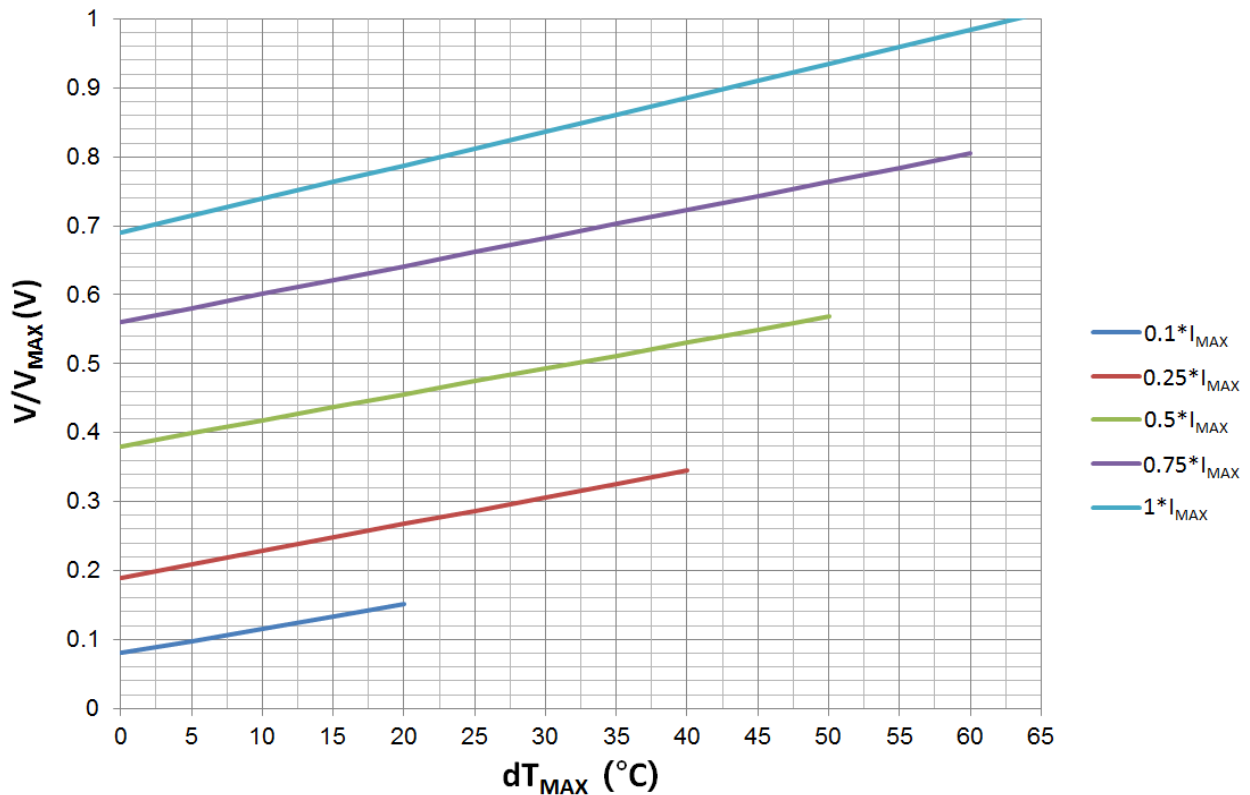


Figure 7. V/V_{MAX} vs. dT_{MAX}

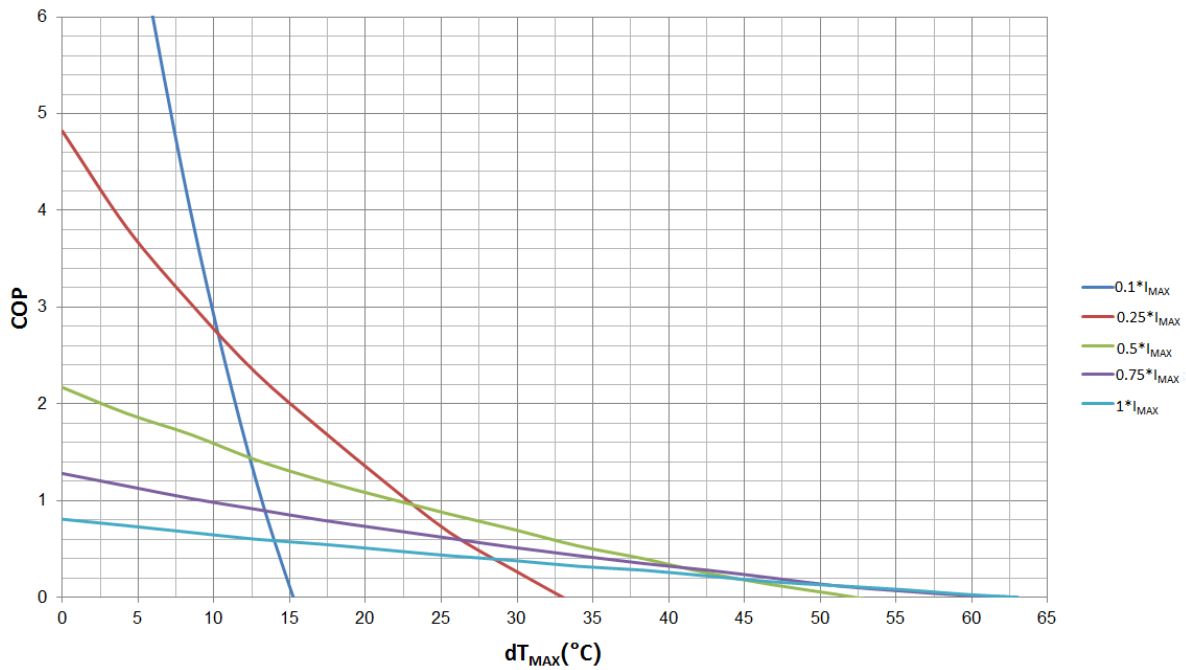


Figure 8. COP vs. dT_{MAX}

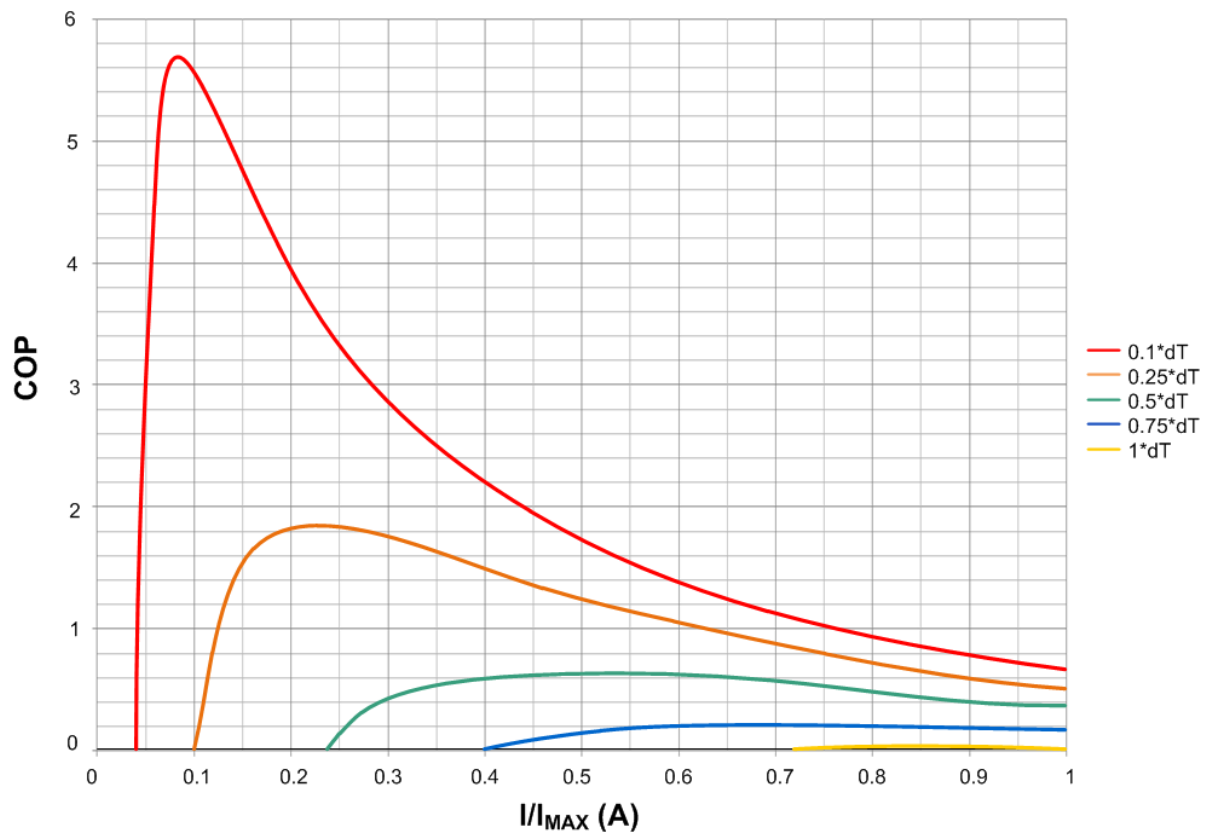


Figure 9. COP vs. I/I_{MAX}



3. $dT_{MAX} = 63^{\circ}C$

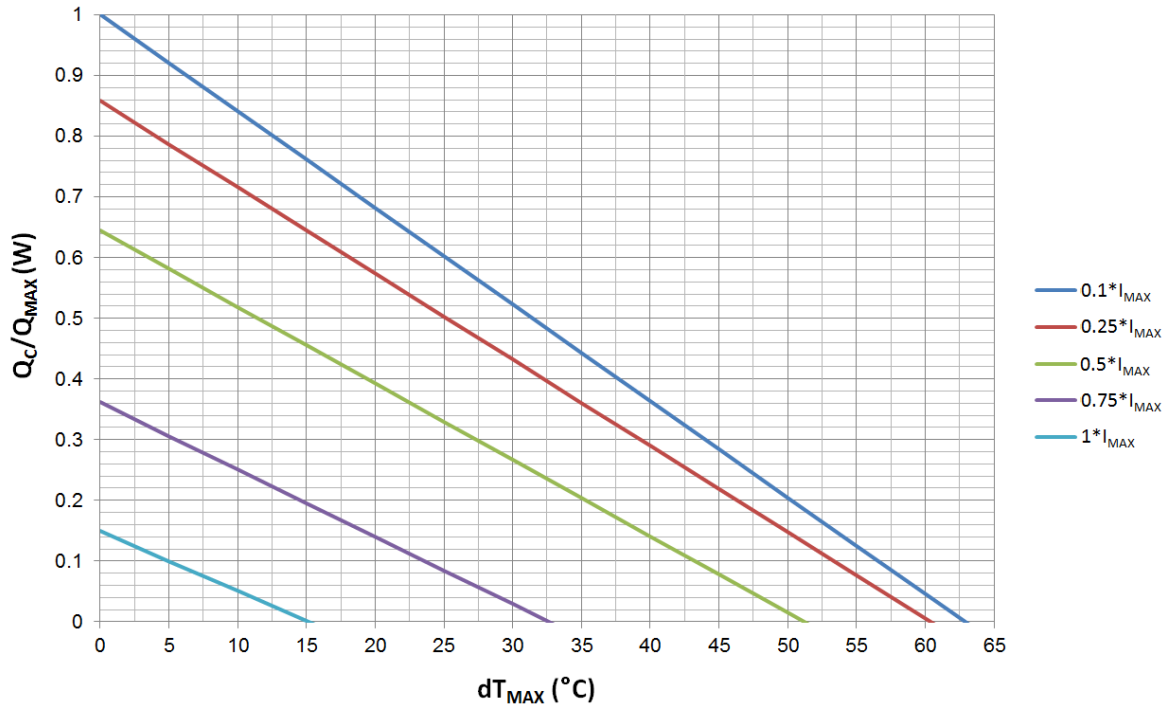


Figure 10. Q_c/Q_{MAX} vs. dT_{MAX}

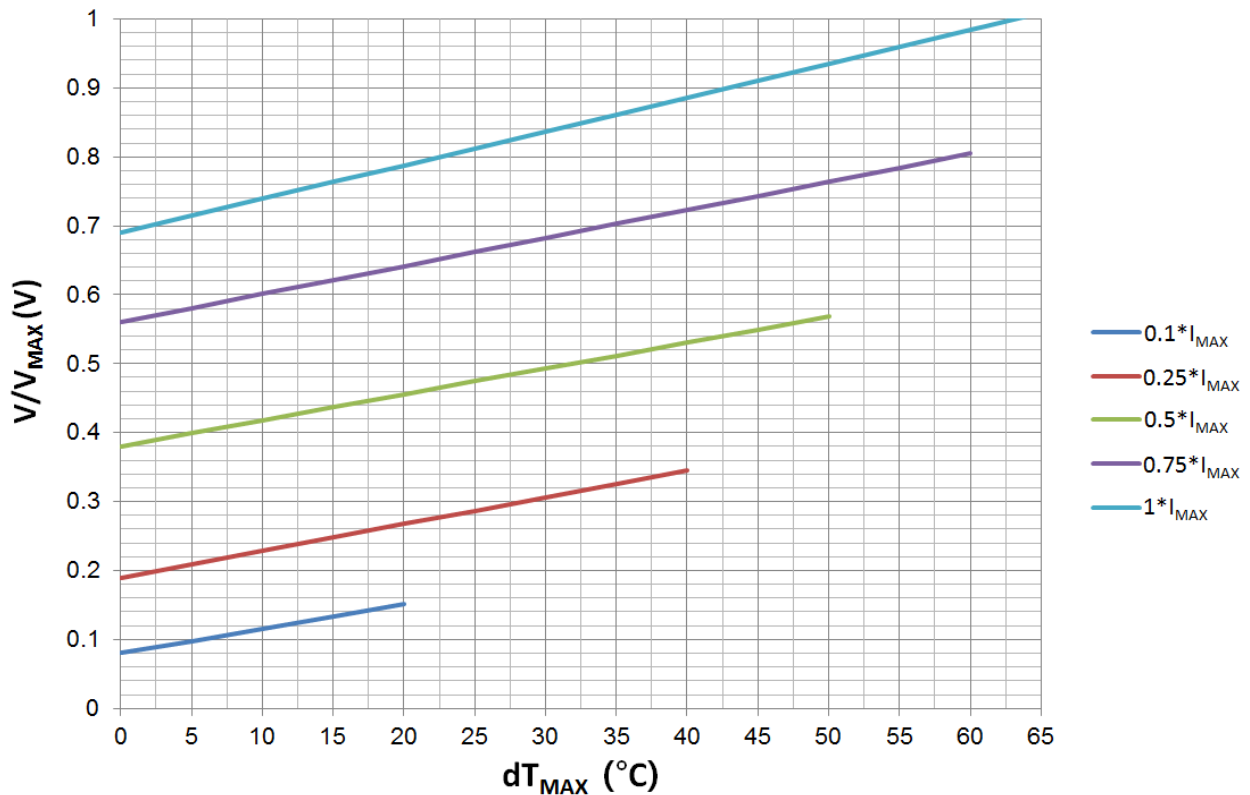


Figure 11. V/V_{MAX} vs. dT_{MAX}

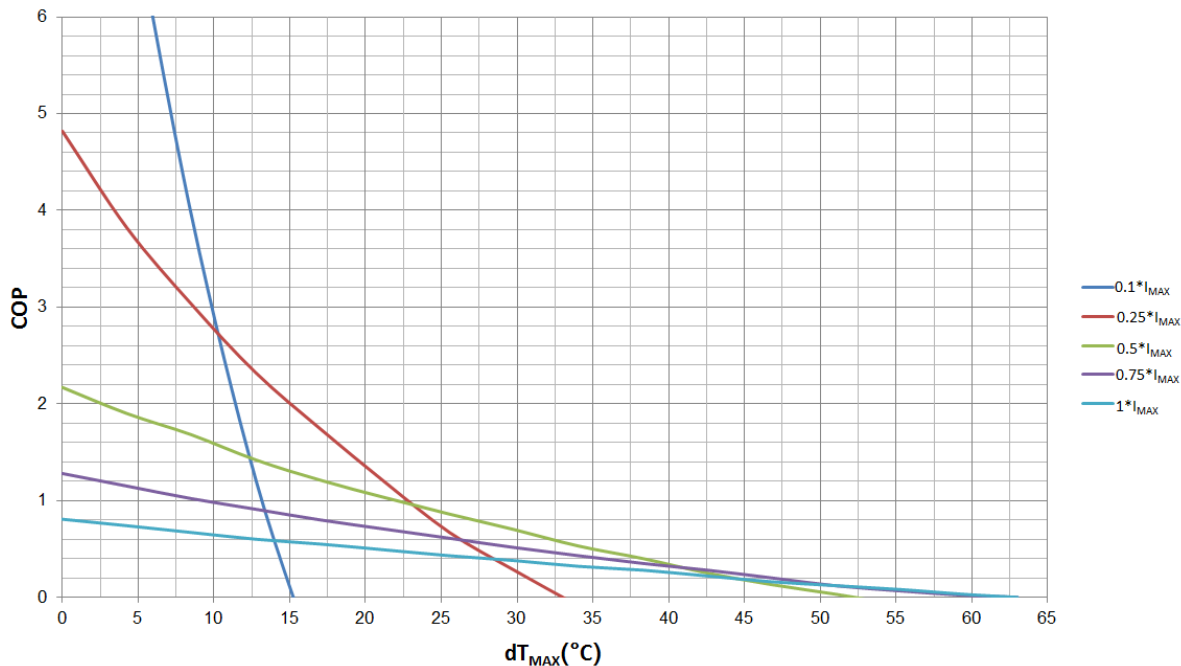


Figure 12. COP vs. dT_{MAX}

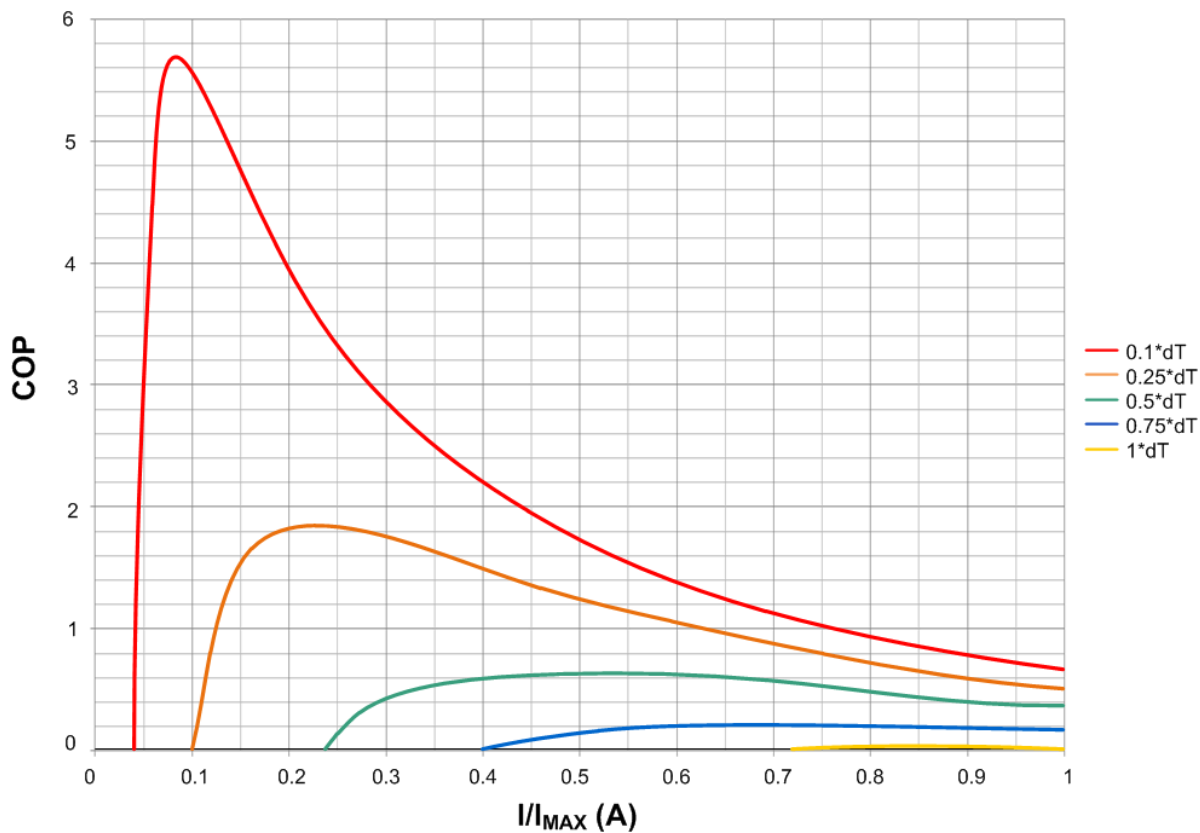


Figure 13. COP vs. I/I_{MAX}



MECHANICAL DIMENSIONS

The mechanical dimensions of the ATE1-71 are shown below.

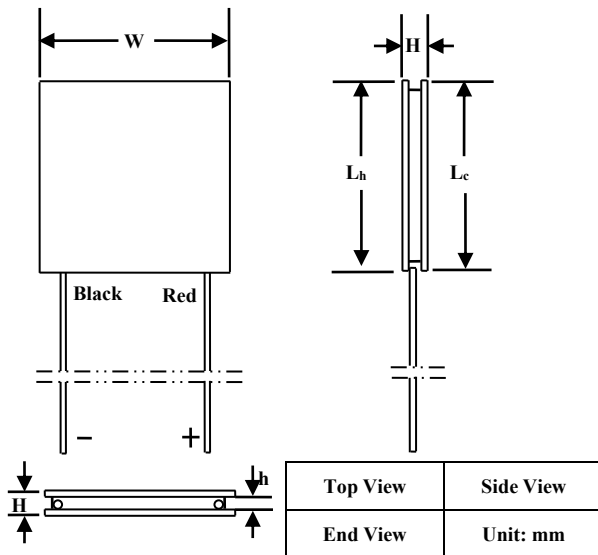


Figure 14. Mechanical Dimensions of Sealed ATE1-71

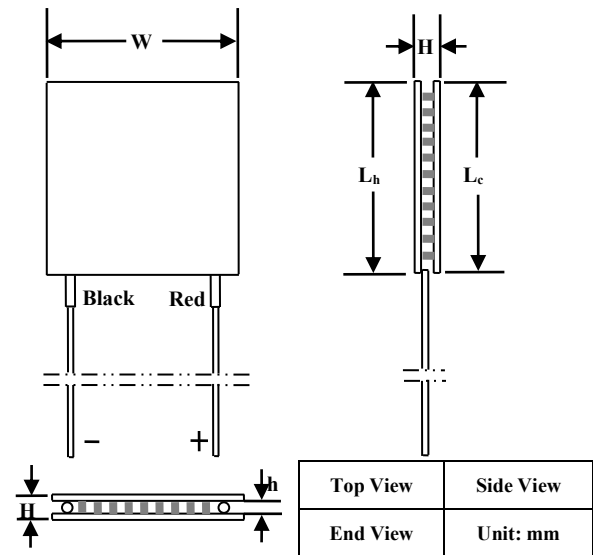


Figure 15. Mechanical Dimensions of Non-sealed ATE1-71

Note: As Figure 14 shows, when the red lead wire is on the right, then the top surface is the cold side of the TEC.

CAUTIONS

1. Never apply electricity to TEC modules without having heat sinks attached properly.
2. Always keep the current less than I_{MAX} , to avoid thermal run-away disaster.

NOTICE

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